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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/824,842

04/14/2004

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6880

29989

7590

06/23/2006

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EXAMINER

BRITT, CYNTHIA H

ART UNIT

PAPER NUMBER

2138

DATE MAILED: 06/23/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/824,842	Applicant(s) LOAIZA ET AL.	
	Examiner Cynthia Britt	Art Unit 2138	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-18 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 April 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>8/5/04, 8/22/05</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claims 1-18 are presented for examination.

Information Disclosure Statement

The information disclosure statements (IDS) submitted on 8/5/04, and 8/22/05 have been considered by the examiner. Forms 1449 have been signed and returned with this office action.

Drawings

The drawings were received on 4/14/04. These drawings are acceptable.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-18 rejected under 35 U.S.C. 102(b) as being anticipated by Koller et al.
U.S. Patent No. 6,009,542.

As per claims 1, 7, and 13 Koller et al. teach the claimed apparatus method and computer instructions for determining a desired location in the nonvolatile memory for

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storing a data block; inserting an address value in the data block, wherein the address value identifies the desired location; prior to performing an operation that stores the data block to the nonvolatile memory, verifying that the address value contained within the data block correctly identifies the location in the nonvolatile memory into which the operation is going to store the data block; and performing the operation to store the data block to the nonvolatile memory only if the address value contained within the data block correctly identifies the desired location in the nonvolatile memory into which the operation is going to store the data block. (Column 1 line 59 through column 2 line 12, Figure 4, column 8 lines 36-47)

As per claims 2, 8, and 14, Koller et al. teach after storing the data block to the nonvolatile memory, reading the data block from a location in the nonvolatile memory; and determining, based upon the address value contained within the data block, whether the data block was read from the desired location in the nonvolatile memory. (figure 4, column 3 line 52 through column 4 line 8)

As per claims 3, 9, and 15, Koller et al. teach the step of determining, based upon the address value contained within the data block, whether the data block was read from the desired location in the nonvolatile memory includes comparing at least a portion of the address value contained within the data block with data that indicates the location in the nonvolatile memory from which the data block was read. (figure 4, column 3 line 52 through column 4 line 8)

As per claims 4, 10, and 16, Koller et al. teach maintaining a mapping that identifies a specific location in the nonvolatile memory into which the data block is to be stored. (column 5 lines 44-62)

As per claims 5, 11, and 17 Koller et al. teach the step of determining a location in the nonvolatile memory includes determining a plurality of locations in the nonvolatile memory for storing the data block; the step of inserting an address value in the data block includes inserting a plurality of address values in the data block, wherein the plurality of address values identify multiple locations in the nonvolatile memory into which the data block is to be stored; and the step of storing the data block to the nonvolatile memory includes storing the data block into each of the multiple locations in the nonvolatile memory only after verifying that the plurality of address values includes an address value that correctly identifies the location in the nonvolatile memory into which the data block is to be stored. (Column 4 lines 9-23)

As per claims 6, 12, and 18, verifying that the address value contained within the data block correctly identifies the location in the nonvolatile memory into which the operation is going to store the data block is performed by a storage device, and the step of performing the operation to store the data block to the nonvolatile memory only if the address value contained within the data block correctly identifies the desired location in the nonvolatile memory into which the operation is going to store the data block is performed by the storage device. (Column 8 lines 36-47)

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

CH 667543 A Data communication for processing network - using stored table in communication unit for each node defining data segment storage location

October 14, 1988 ASCHMANN, HANS-RUDOLF Switzerland

This patent teaches data segments transferred between the spaced data processor nodes. Each of these has a read/write store for holding the data segments and a communication unit connected to an external data channel. Each communication unit has a memory holding a table which identifies the base address and size of the data segments in the read/write store for location of the data segments to be transmitted and for direct insertion of the received data in the correct location of the read/write store.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cynthia Britt whose telephone number is 571-272-3815. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


Cynthia Britt
Examiner
Art Unit 2138